## **Energy Harvesters and Energy Processing Circuits**

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### **Trends in low-power electronics**



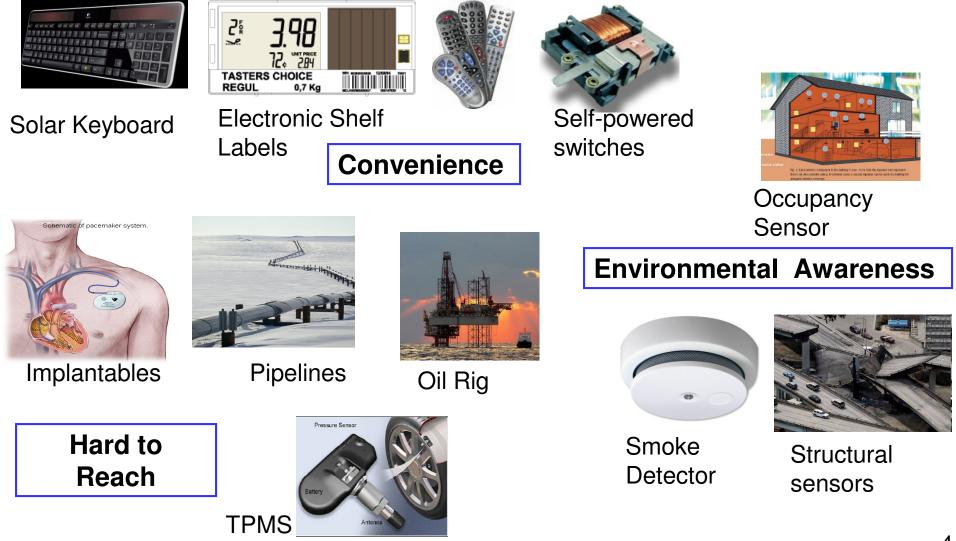
- Issues Size, weight, operating lifetime
- Energy efficiency of IC's is crucial

## **Sensor Node for Monitoring**

Component	Power	Comments	Sensors Energy Subsystem	
Inst. Amplifier [Verma, VLSI09]	3.5µW	1V V <sub>DD</sub> , 1.3μVrms input referred noise	Interface & DC-DC	
ADC [Agnes, ISSCC08]	3.8µW	1V V <sub>DD</sub> , 100kS/s, 9.4ENOB	Analog Front-End Analog Front-End Processor RAM CPU CPU	
16b μ-cont [Kwong, ISSCC08]	2.72µW	0.5V V <sub>DD</sub> ,128kb SRAM, 100kHz	<ul> <li>Power consumption of building blocks steadily decreasing</li> </ul>	
Radio [CC 2550]	33.6mW (active)	3V V <sub>DD</sub> , 2.4GHz, -12dBm P <sub>OUT</sub>	<ul> <li>Low voltage operation, multi-cores, local processing of information,</li> </ul>	
			aggressive duty cycling	

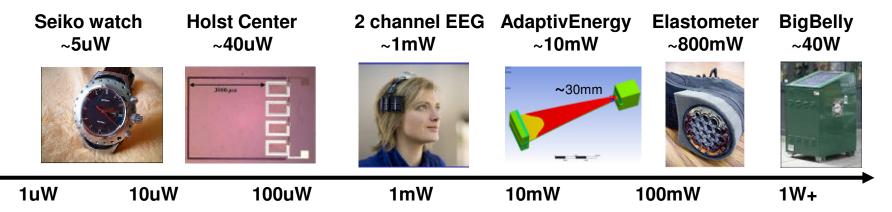
### **Self-Powered Applications**

#### Low data rate, low duty cycle, ultra-low power

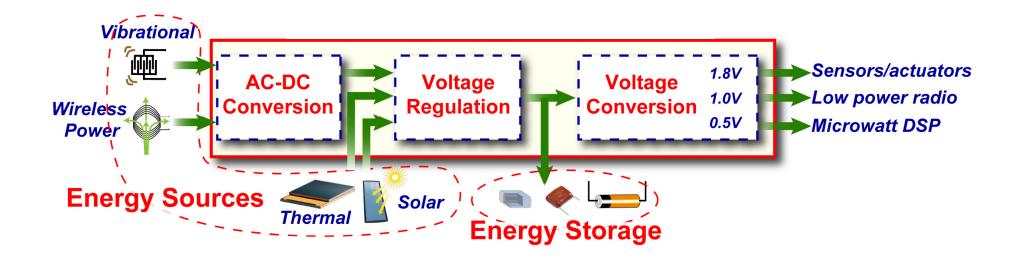


## **Energy Harvesting Sources**

Energy Source	Characteristics	Efficiency	Harvested Power
Light	Outdoor Indoor	10~24%	10 mW/cm² 10 μW/cm²
Thermal	Human Industrial	~0.1% ~3%	60 μW/cm² ~1-10 mW/cm²
Wireless	Near field Far field	> 60% < 1%	1-10 mW/cm² <10 μW/cm²
Vibration	~Hz—human ~kHz—machines	25~50%	~4 μW/cm³ ~800 μW/cm³

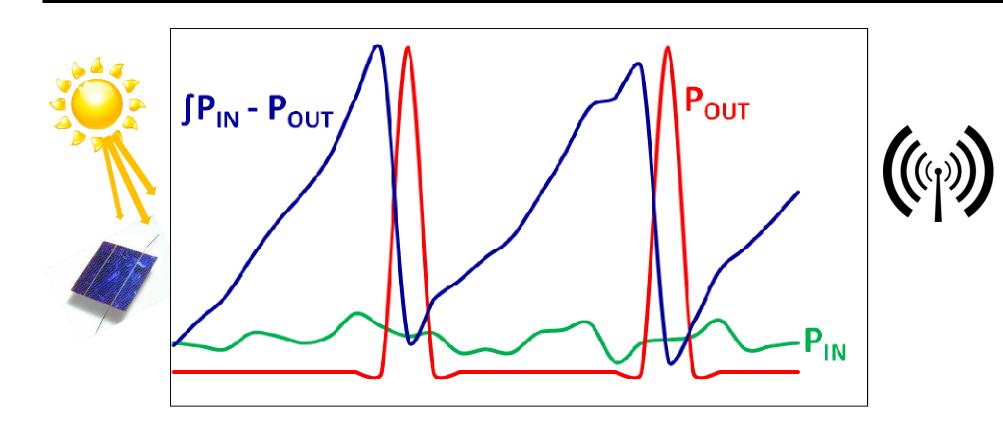


## **Energy Processor**



- Low-power applications  $\rightarrow$  less than 10mW
- Efficiency of power delivery a key bottleneck
- Amount of power obtained better metric for energy harvesters

#### **Need for Energy Buffer**



- Accumulate input power
- Provide peak output power
- Smooth out input, output power imbalances

#### **Energy Storage Options**

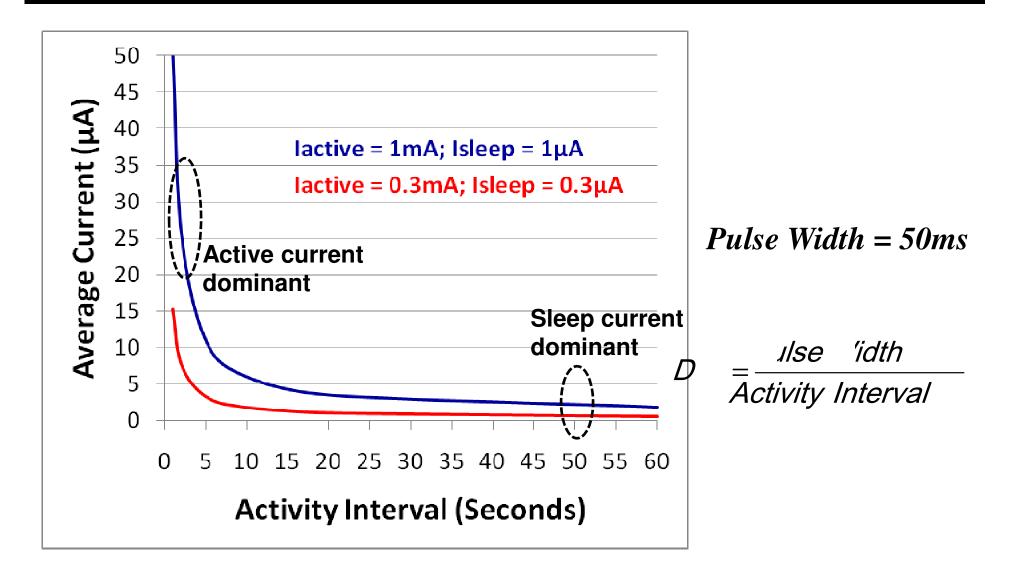
	Conventional Batteries	Thin Film Batteries	Supercaps
Recharge Cycles	100s	5k-10k	Millions
Self Discharge	Moderate	Negligible	High
Charge Time	Hours	Minutes	Sec-Minutes
Impedance	Low - High	High	Low
Physical Size	Large	Small	Medium
Capacity	0.3-2500mAH	12-2200µAH	10-100µAH





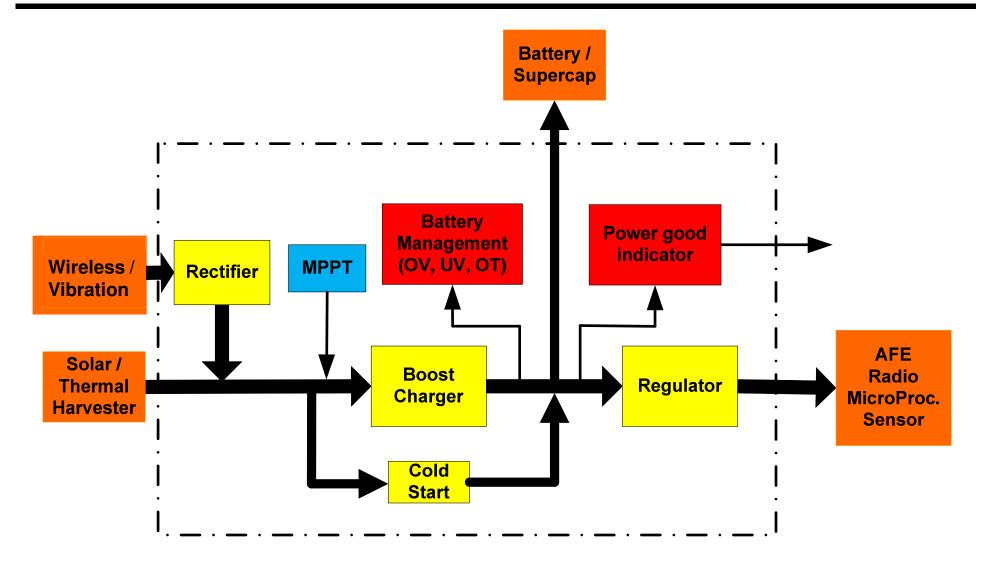


#### **Duty Cycle Impact on Current**



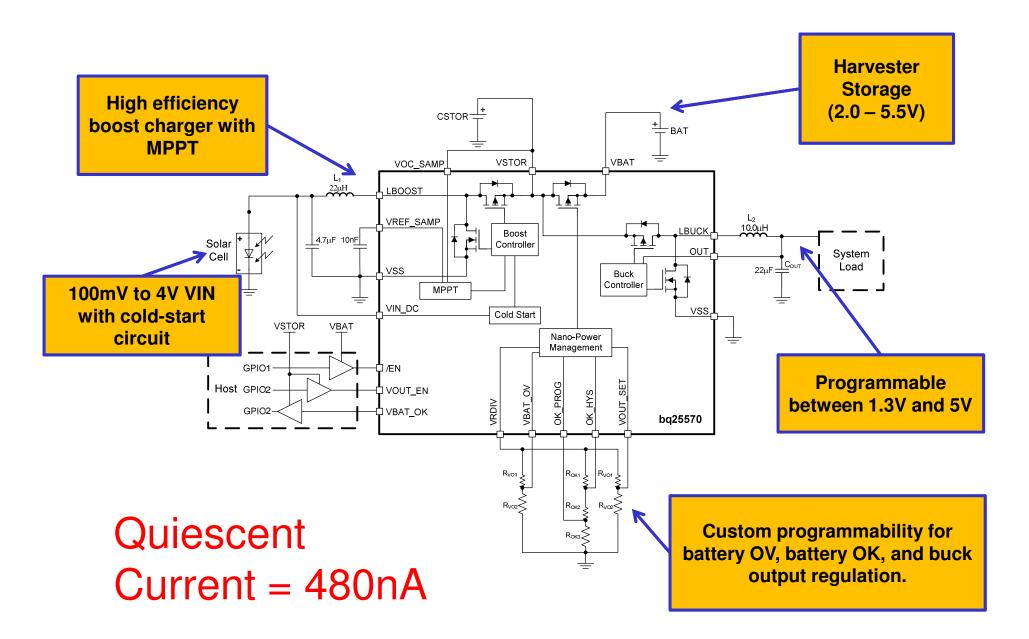
I(avg) = Iactive \* D + Isleep \* (1-D)

## **Energy Harvesting System**

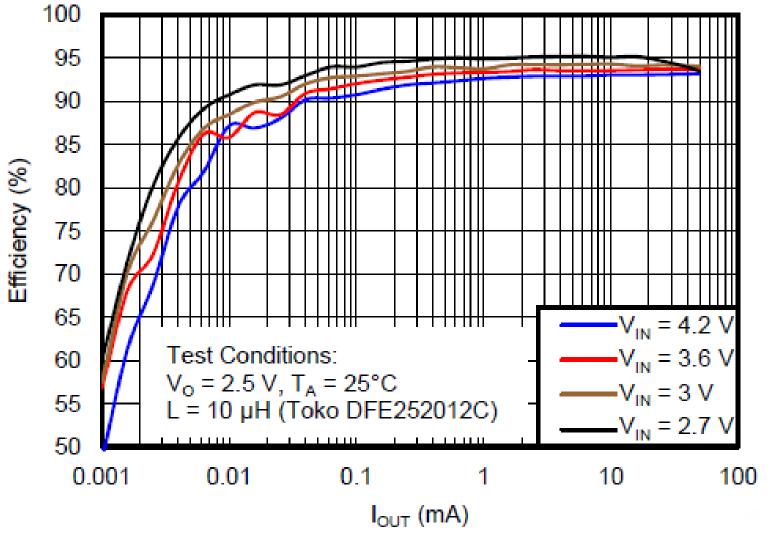


#### **Energy harvesters are power sources**

# Energy Mgmt. IC -BQ25570



## **Buck Converter Efficiency**



• Efficiency vs.  $I_{OUT}$ ,  $V_{OUT} = 2.5V$ 

# Summary

- Advances in circuit design techniques and architectures have made it possible for electronic systems to be completely self-powered
- Energy harvesting sources differ in characteristics from conventional batteries requiring specialized interface circuits
- Optimized energy processing circuits are crucial to manage the ultra-low power levels output by energy harvesters